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phase locked loop and phase frequency detector and D flip flop

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1 A high speed and low power phase-frequency detector and charge-
Won-Hyo Lee; Jun-Dong Cho; Sung-Dae Lee;
Design Automation Conference, 1999. Proceedings of the ASP-DAC '99. Asia
Pacific , 18-21 Jan 1999
Page(s): 269 -272 vol.1

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 - ⊖ L3: (677) 1 and (phase adj frequency adj detector)
 - ⊖ L4: (65) 3 and d adj flip adj flop
 - ⊖ Failed
 - ⊖ (0) 4 and OR adj gate
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